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FY26 第二季 法人說明會

August 2026

免責聲明

本簡報包含關於 Taiwan Semiconductor Inc. (TSC) 業務前景、策略規劃、產品開發及市場狀況的前瞻性陳述。這些陳述反映了管理層當前的預期和假設，本質上存在重大風險和不確定性。由於包括但不限於全球經濟形勢變動、客戶需求演變、監管發展、供應鏈限制及競爭動態等因素，實際結果可能與預測存在重大差異。

關於未來產品功能、時程或可用性的資訊僅供參考，不構成承諾。TSC 保留自行決定修改、推遲或取消產品計劃的權利。除非適用法律要求，否則 TSC 不承擔因新資訊、未來事件或其他原因而更新任何前瞻性陳述的義務。

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References to future product features, timelines, or availability are provided for informational purposes only and do not constitute a commitment. TSC reserves the right to revise, delay, or cancel product plans at its sole discretion.

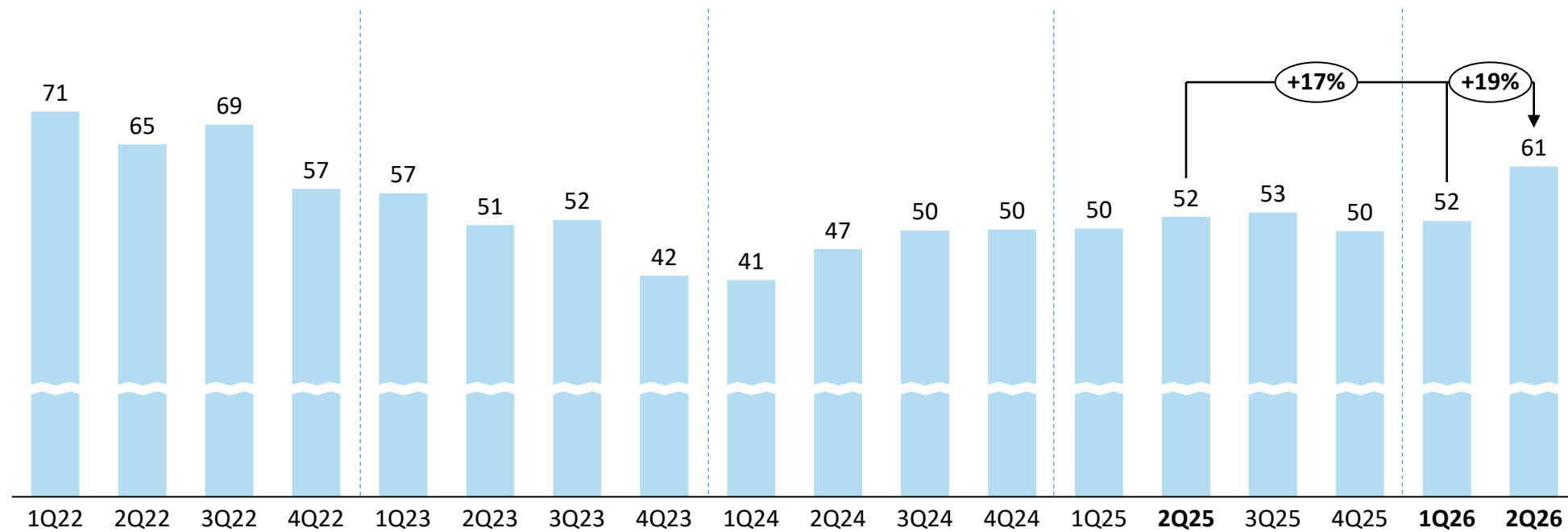
Taiwan Semiconductor Inc. and its subsidiaries have no intention and undertake no obligation to update any forward-looking statement, whether as a result of new information, future events, or otherwise, except as required by law.

財務資訊
2Q26

Taiwan Semiconductor Co., Ltd

季度營收 (半導體)

Unit: \$USD M

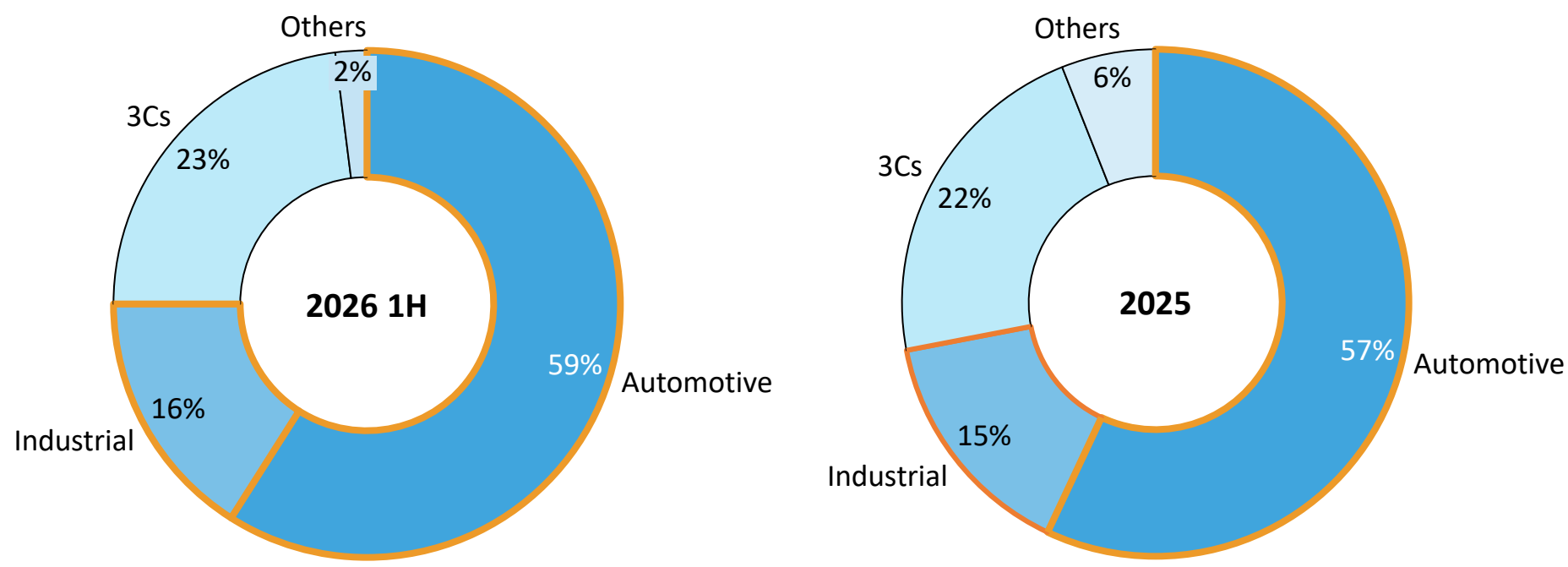


損益表
(簡表)

In NTD millions	TSC Semi (5425)					TSC Group				
	2026Q2 YTD		2025Q2 YTD		YoY	2026Q2 YTD		2025Q2 YTD		YoY
	Amnt	%	Amnt	%	%	Amnt	%	Amnt	%	%
Net Revenue	\$ 3,561	100	\$ 3,279	100	9	\$ 9,471	100	\$ 9,216	100	3
Gross Margin	958	26.9	742	22.6	4.3 pts	2,879	30.4	2,612	28.3	2.1 pts
OPEX	655	18.4	592	18.0	0.3 pt	2,028	21.4	1,866	20.3	1.2 pts
Ops Income	304	8.5	151	4.6	101	851	9.0	746	8.1	14
Non-Ops	155	4.3	78	2.4	98	28	0.3	(159)	(1.7)	(118)
Tax	86	2.4	71	2.2	20	211	2.2	163	1.8	30
Non-Control	-	0.0	-	0.0	-	296	3.1	267	2.9	11
Net Income	\$ 373	10.5	\$ 158	4.8	5.7 pts	\$ 373	3.9	\$ 158	1.7	2.2 pts
EPS (NT\$)	\$ 1.52		\$ 0.64			\$ 1.52		\$ 0.64		

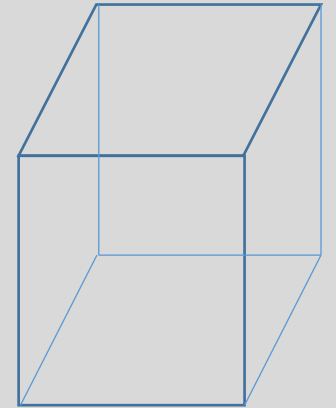
營收分佈 – 應用

Auto + Industrial **72~75%** of Revenue

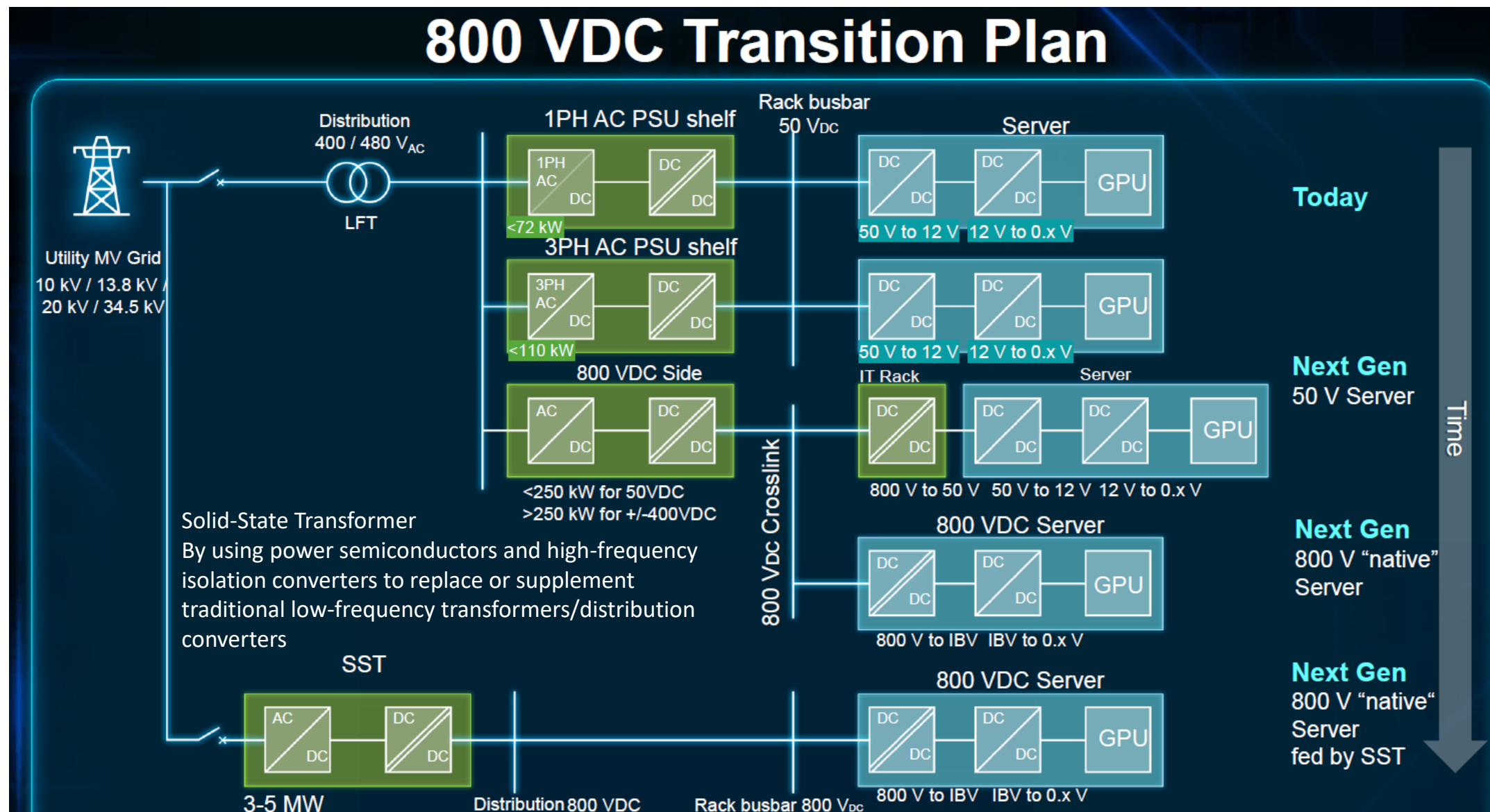


Taiwan Semiconductor Co., Ltd

補充資料

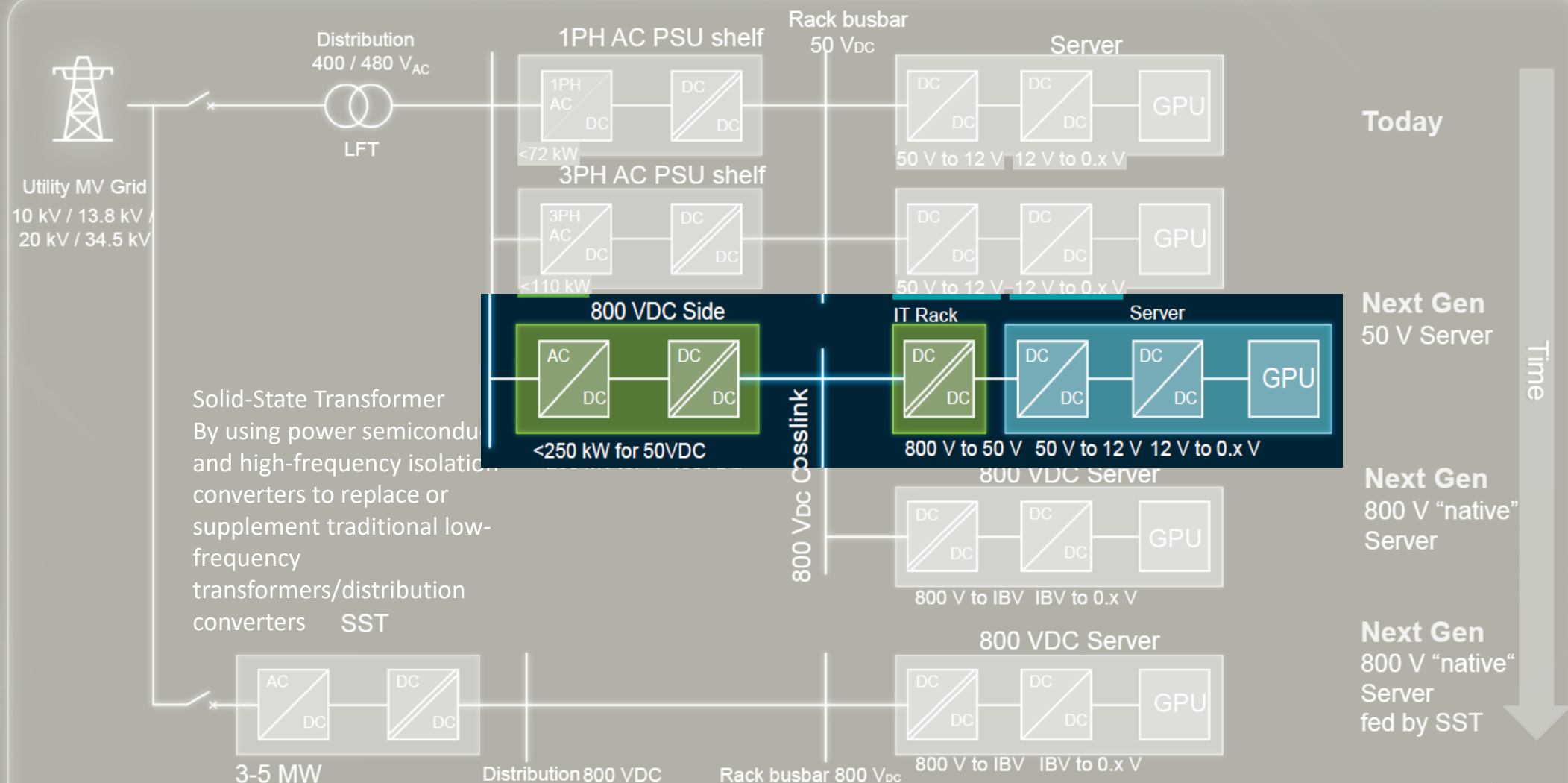


800V HVDC TRANSITION PLAN

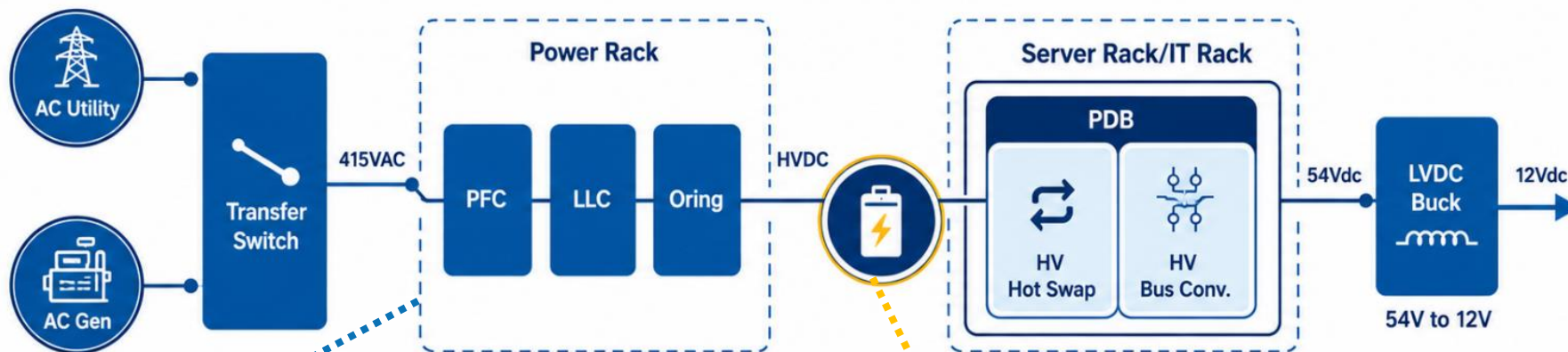


POWER RACK AND BBU(DC/DC)

800 VDC Transition Plan



POWER RACK AND BBU(DC/DC)



Power Rack (3 Φ 380VAC~480VAC $\rightarrow$ AC/DC(+/-400VDC or 800VDC))

Vienna or T-type PFC

*400V-SiC MOSFET: 650V/23m Ω /H-PDSO-F8(9.9mmx11.68mm) \$3.8 and 1200V/40m Ω /D2PAK-7 \$4.8

*800V-SiC MOSFET: 1200V/20m Ω /D2PAK-7 \$5.8 and 1200V/12m Ω /D2PAK-7 \$6.2

3 phase LLC

*400V-SiC MOSFET: 1200V/30m Ω /D2PAK-7 \$5.2

*800V-SiC MOSFET: 1200V/12m Ω /D2PAK-7 \$6.2

Bootstrap:

*1200V-SiC Schottky: 1200V/1~2A/SMB,SOD-128 \$0.03

SR:

*400V-SiC MOSFET: 650V/45m Ω /DFN88 \$3.5

*800V-SiC MOSFET: 1200V/12m Ω /D2PAK-7 \$6.2

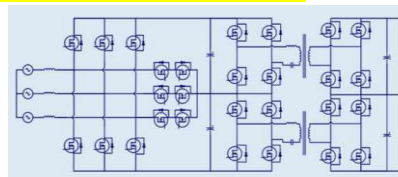
Oring:

*400V and 800V- SiC MOSFET: 750V/7m Ω /H-PDSO-F8(9.9mmx11.68mm) \$5.5

Standby Power:

*400V Primary Side: 650V/25m Ω /TOLL GaN MOSFET

*400V Secondary Side: 80V/1.9m Ω /DFN56 \$0.65



SiC

TSC opportunity

HV BBU Rack (+/-400VDC or 800VDC $\rightarrow$ 48VDC~54VDC)

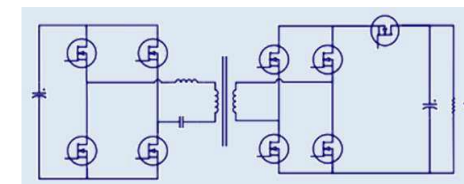
BMS

*C/DFET: SJ 650V/18m Ω /TOLL

DC/DC

*LLC: GaN 100V

*SR: GaN 100V



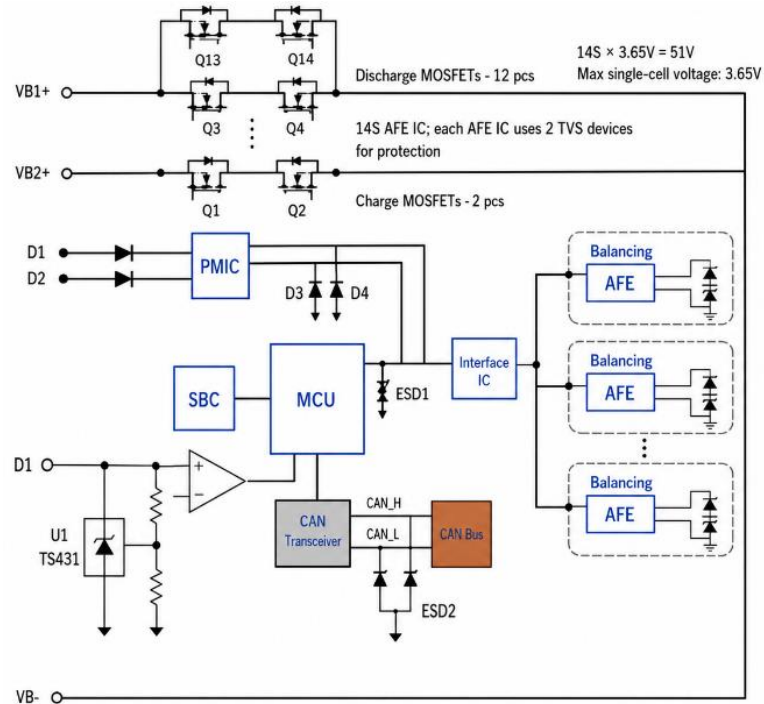
SJ

GaN

BBU=BMS(Next page)+DC/DC, DC/DC used GaN MOSFET to design

TSC AVAILABLE FOR OF BBU(BMS)

TSC opportunity are TVS and ESD



Q1-Q4 (NMOS*14PCS) :

INFINEON IPT65R018CM8 (650V 134A 18mΩ Toll)

TSC NMOS: NA

TVS1-TVS2 (*28PCS) :

400W 85V SMA-Uni

TSC : SMAJ85A \$0.03

D3&D4(SKY*2PCS) :

TSC : TSSE3U60 \$0.05

ESD1: (*8PCS)

ESD /5V/ SOD323

TSC : TESDL5V0B45P1M3 \$0.028

U1-U2 (*2PCS) :

TL431 2.5V SOT-23

TSC : TS431BCX \$0.04

ESD2 : (Can Bus ESD*2PCS)

ESD 24V SOT-23

TSC : TESD24VS2BT(24V SOT-23) \$0.05

ESD1 : (*7PCS)

ESD /3.3V/ SOD323

TSC : NA

Q15 (Linear regulator NMOS*1PCS) :

INFINEON IPD70R600P7S (700V 8.5A 0.6Ω DPAK)

TSC NMOS: NA

ZD1(Zener*1PCS) :

12V 0.5W SOD-123F

TSC : BZT52C12 \$0.02

ESD1 : (*6PCS)

ESD /15V/ SOD323

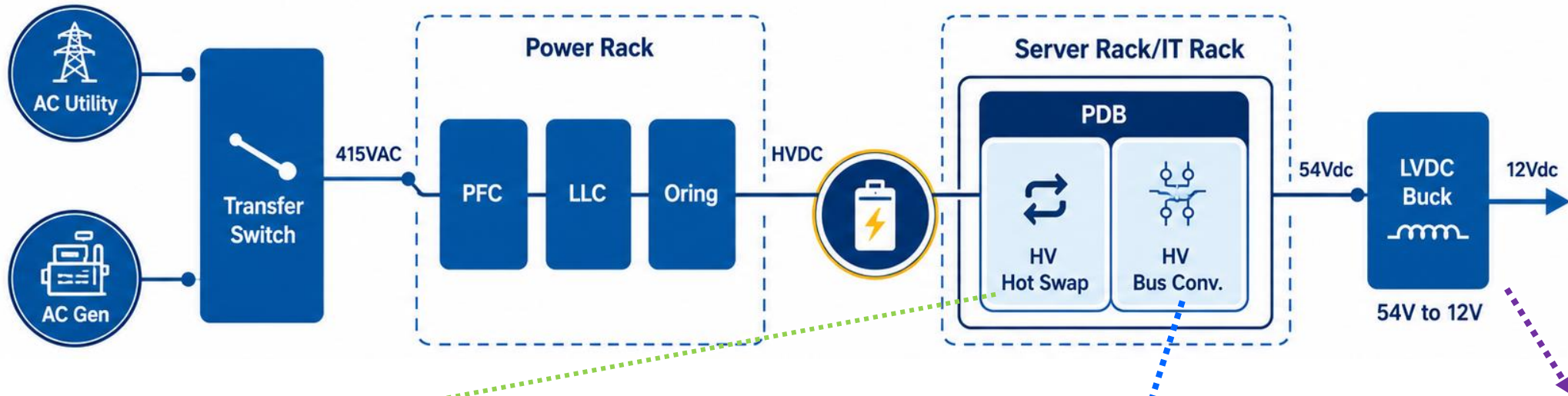
TSC : NA

D1&D2(SKY*2PCS) :

SKY 100V 1A SOD-123

TSC : SS110LW (100V 1A SOD-123W) \$0.05

HV HOT SWAP / BUS CONVERTER / LVDC BUCK



HV HOT SWAP (+/-400VDC or 800VDC)

Compute Tray

*400V-SiC 650V/10mΩ~15mΩ/TO247/TOLL \$5.8

*800V-SiC

1200V/10mΩ~30mΩ/TO-247/TOLL \$6.2

Switch Tray

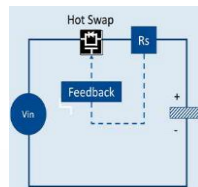
*400V-HVSI: 600V/35mΩ/TOLL \$3.5

* 800V-SiC

1200V/10mΩ~30mΩ/TO-247/TOLL \$6.2

**Switch Mode: IFX/TI

**Linear Mode: ADI/MPS



HVDC Bus Conv.(+/-400VDC or 800VDC → 48VDC~54VDC)

Power Shelf (400V to 54V)

1'ST *400V-GaN 650V/30mΩ/TOLL

2'ND*400V-SGT

80V/1.9mΩ/DFN56 \$0.65

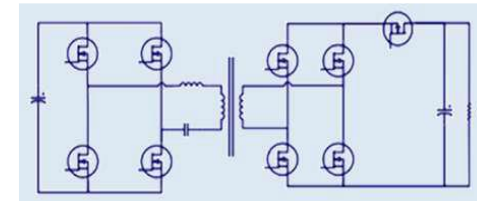
Power Shelf (800V to 54V)

1'ST *800V-

SiC 1200V/10mΩ/TOLL \$6.2

2'ND*800V-

SGT 80V/1.9mΩ/DFN56 \$0.65



LVDC Buck (48VDC~54VDC → 12VDC)

Brick Power

*Prim Side / High / Mid Side:
80V/2mΩ~4.6mΩ/DFN3x3SDCG
(Source down center gate package)

*Secondary Side / Low Side:
40V/1.35mΩ/DFN3x3SDCG
(Source down center gate package)



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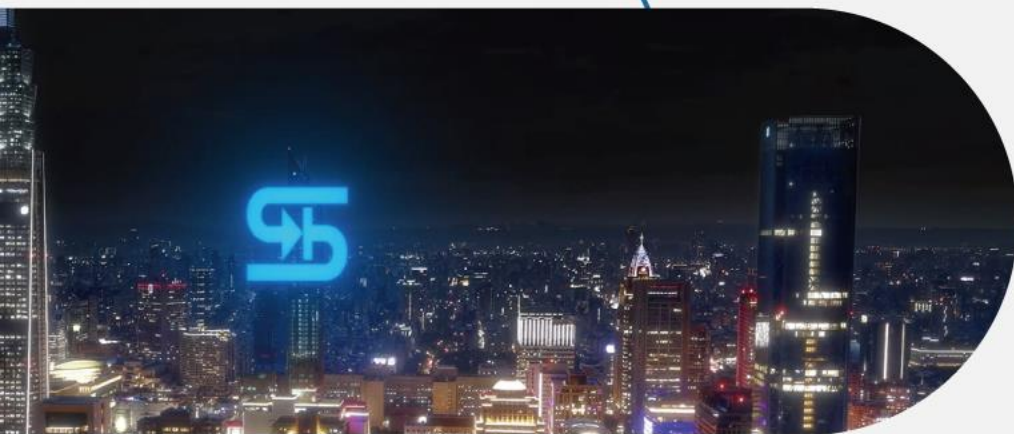
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